

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Danilak, R.

Serial No. : 10/725,980 Examiner: Lee, C.

Filed : 12/01/2003 Group Art Unit: 2182

For : A BYPASS METHOD FOR EFFICIENT DMA DISK I/O

RESPONSE TO OFFICE ACTION

Commissioner for Patents & Trademarks P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed 5/15/06, please reconsider the above referenced application in view of the following remarks:

Examiner: Lee, C.

Art Unit: 2182